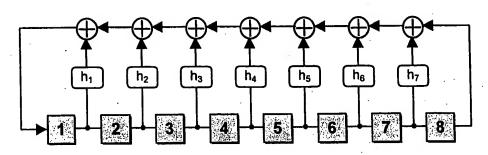
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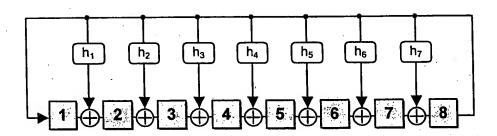
TYPE I LFSR



PRIOR ART

FIG. 1

TYPE II LFSR



PRIOR ART

FIG. 2

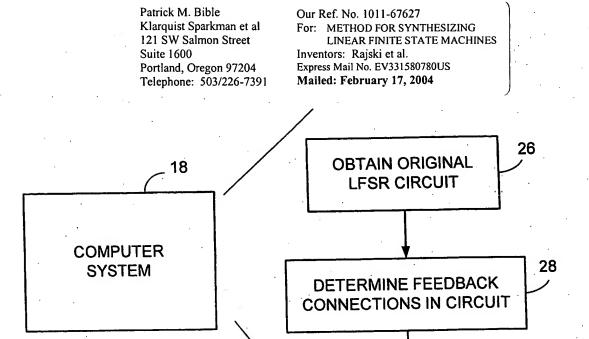


FIG. 3

SHIFT FEEDBACK CONNECTION(S) ACROSS A NUMBER OF MEMORY ELEMENTS IN LFSR CIRCUIT IN THE SAME DIRECTION

30

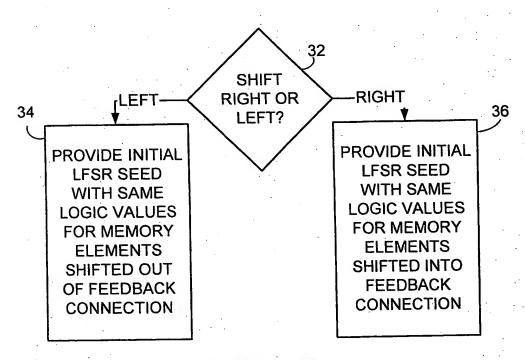


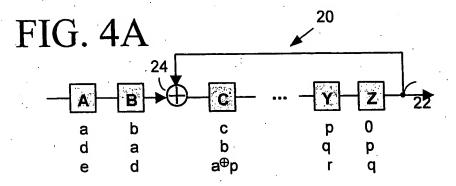
FIG. 5

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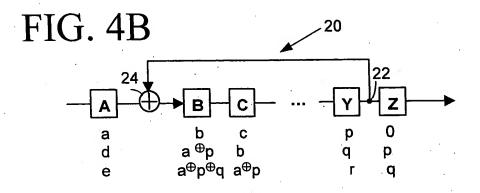


FIG. 6A

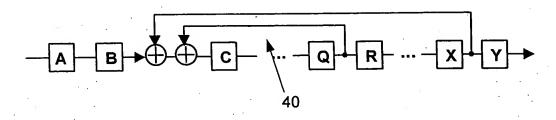
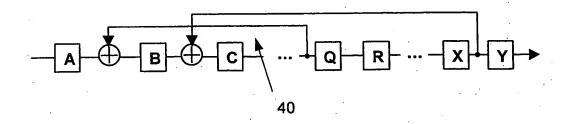
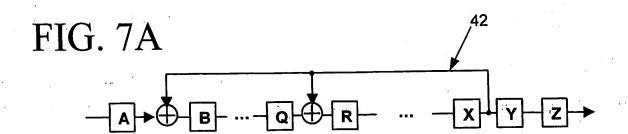


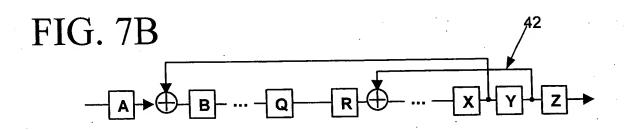
FIG. 6B

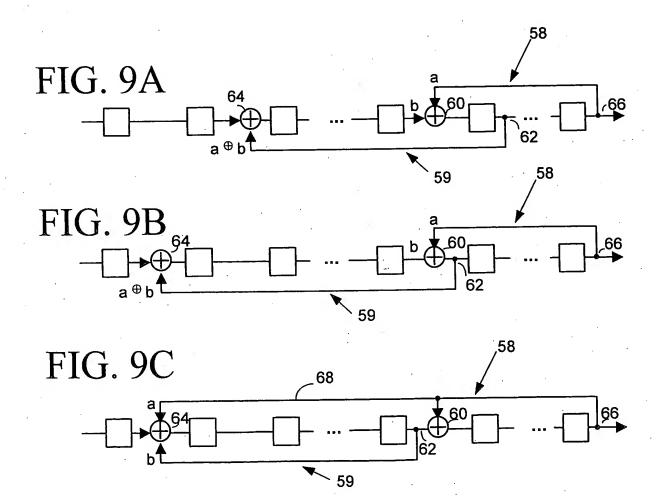


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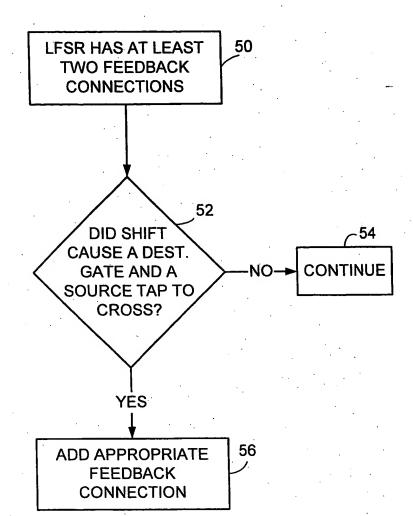
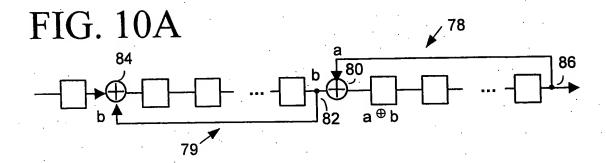


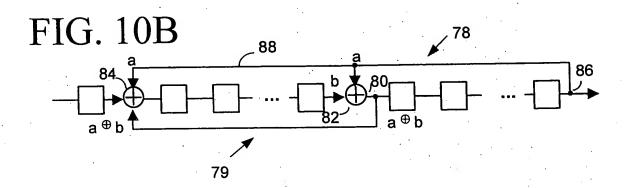
FIG. 8

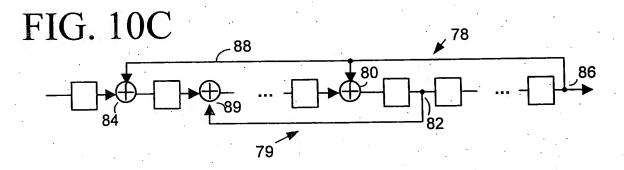
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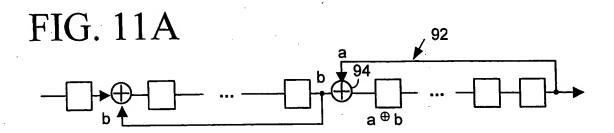


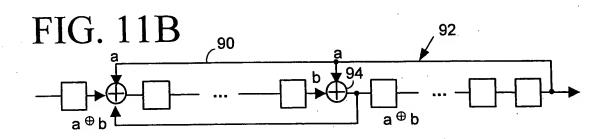


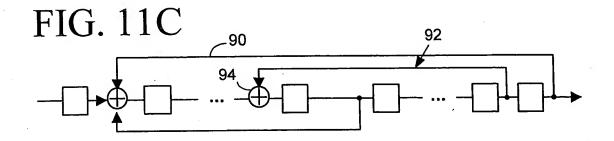
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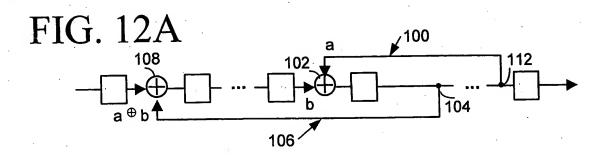
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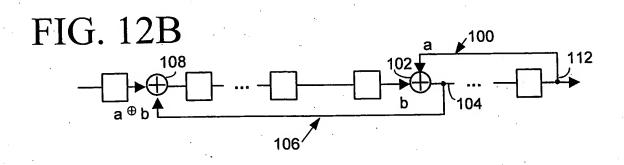


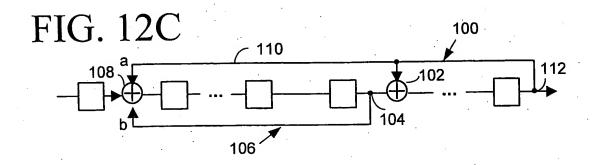




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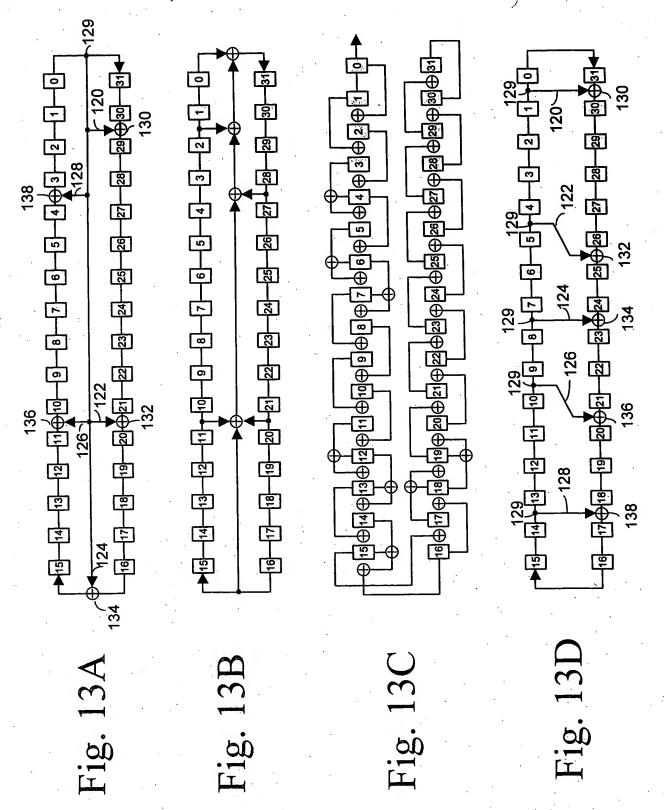


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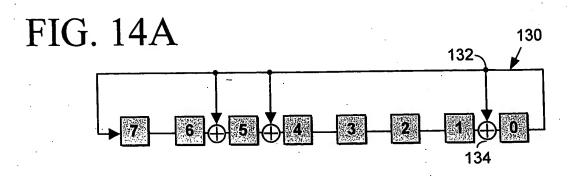


FIG. 14B

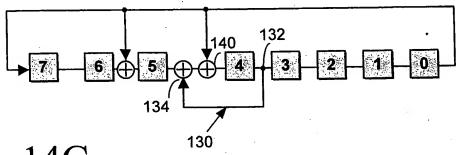


FIG. 14C

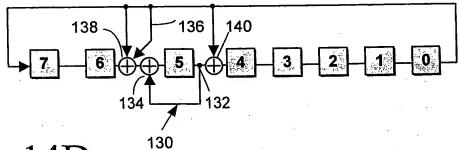


FIG. 14D

